

ABSTRACT

The solar energy conversion system is an alternative for conventional power generating system. It has no running cost due to freely available and non polluting solar radiations. The voltage which is available from solar array is variable and to obtain a stable voltage from solar panels, DC-DC converters are required for constant power production. There are mainly three converters namely Buck, Boost and Buck-Boost converters which can be used for either increasing or decreasing the voltage.

This paper presents mobile charging circuit with a PV source. The circuit structure of the proposed system adopts buck converter combined PWM MPPT technique. In this research, buck converter is used as a charger for charging mobile battery. The input voltage can typically change from (12V) initially, down to (5V), and provide a regulated voltage within the range of the 4.5V required for the charging of mobile batteries.

KEYWORDS: Buck converter, MOSFET, Mobile charging system, MPPT technique, PV system.

INTRODUCTION

DC-DC converters are considered to be of great economical importance in today's society, and are perhaps one of the few electronic circuits that are commonly used in switching power supplies, generally are widely used at home solar systems to produce the desired output power[6]. More sophisticated applications require electronic converters to process the electricity from the PV device. These converters may be used to regulate the voltage and current at the load, control the power flow in grid-connected systems, and mainly track the maximum power point (MPP) of the device[10]. Therefore, this paper tries to study an electronic converter in PV systems, namely the Buck converter, and propose an easy way to electronic converter designers to calculate component values needed to realize it.

DC-DC Converters

There are three kinds of switching mode DC-DC converters, buck, boost and buck-boost. The buck mode is used to reduce output voltage, whilst the boost mode can increase the output voltage. In the buck-boost mode, the output voltage can be maintained either higher or lower than the source but in the opposite polarity. The simplest forms of these converters are schematically represented in Figure 1, 2 & 3. These converters consist of the same components, an inductor, L, a capacitor, C and a switch, which has two states $u = 1$ and $u = 0$. All converters connect to a DC power source with a voltage (unregulated), V_{in} and provide a regulated voltage, V_o to the load resistor, R by controlling the state of the switch. In some situations, the load also could be inductive, for example a DC motor, or approximately, a current load, for example in a cascade configuration. For simplicity, here, only current and resistive loads are to be considered.

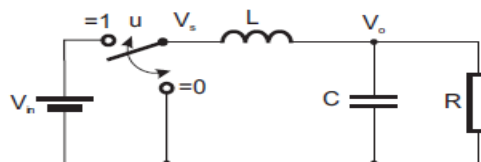


Figure:1 Buck Converter

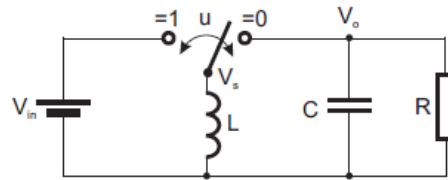


Figure:2 Boost Converter

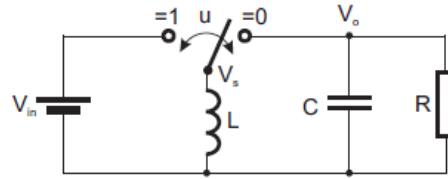


Figure:3 Buck- Boost Converter

Working Principles:

The working principles of these DC-DC converters can be explained as follows.

In the buck mode, when the switch is on position 1, the DC source supplies power to the circuit which results in an output voltage across the resistor. When the switch changes its position to 0, the energy stored in the inductor and capacitor will discharge through the resistor. Appropriately controlling the switching position can maintain the output voltage at a desired level lower than the source.

In the boost mode, when the switch is on position 1, the circuit is separated into two parts: on the left, the source is charging the inductor, meanwhile the capacitor on the right maintains the output voltage using previously stored energy. When the switch changes its position to 0, both the DC source and energy stored in the inductor will supply power to the circuit on the right, hence boost the output voltage. Again, the output voltage can be maintained at a desired level by controlling the switching sequence.

Finally, for the buck-boost mode, switch positions 1 and 0 represent charging and discharging modes of the inductor. Appropriately controlling the switching sequence can result in an output voltage higher or lower than the DC source. Since the inductor cannot change the direction of current, the output voltage is opposite to the DC source.

This paper provides the designing and simulation of a Buck converter. The reason to choose a Buck converter is that our load is a 4.5V mobile battery, where we will charge it with only one PV module which generates the most watts running at around 12 V. Thus, we need a DC/DC converter which takes a higher input voltage and converts it to a lower output voltage. This can only be done by the Buck converter. We will, first, start with a brief introduction to the functioning of a Buck converter, and then, write its basic equations.

WORKING OF BUCK CONVERTER IN BRIEF**Circuit description:**

The basic circuit of a Buck converter is shown below in Figure 4. Two switches are used in this basic circuit, usually one controlled (MOSFET) and one uncontrolled (diode), to achieve unidirectional power flow from the input to the output. This circuit, also, uses one capacitor and one inductor to store and transfer energy from input to output. They, also, filter or smooth voltage and current [10].

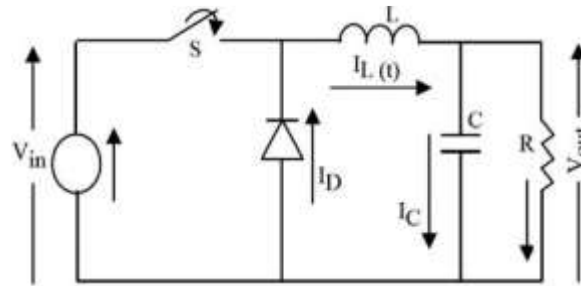


Figure:4 Basic circuit of a Buck converter

Circuit operation:

We used inductors and capacitors as energy storage components to control the energy flow from the input to the output by continuously opening and closing the switch which is usually an electronic device that operates in two states: When the switch is ON for a time duration αT (α is the duty cycle), the switch conducts the inductor current and the diode becomes reverse biased. This results in a positive voltage $V_L = V_{in} - V_{out}$ across the inductor. This voltage causes a linear increase in the inductor current I_L . When the switch is turned OFF, because of the inductive energy storage, I_L continues to flow. This current now flows through the diode, and $V_L = -V_{out}$ for a time duration $(1 - \alpha)T$ until the switch is turned ON again.

Analytical expressions:

The basic circuit of the Buck converter showed in Figure 1 has two energy storage elements: the inductor and capacitor which can be calculated in a second order differential equation. We start by the differential equation for the capacitor voltage, in the ON state, which given by:

$$L \times C \frac{d^2V_c}{dt^2} + \frac{L}{R} \times \frac{dV_c}{dt} + V_c(t) = V_{in}$$

Assumption: $V_c(t)$ is constant, so the differential equation above can be simplified and can be written for the current through the inductor as:

$$L \times \frac{dI_L(t)}{dt} = V_{in} - V_{out}$$

For a steady state, and when $t \rightarrow T_{ON} = \alpha T$ the inductor current attains it final

$$\text{value: } I_L = \frac{V_{in} - V_{out}}{L} \times \alpha T + I_{L0}$$

Where I_{L0} is the initial value of the inductor current (just before the switch is turned on). The peak-to-peak current ripple ΔI_L is the difference between the final and initial value of I_L and is given by:

$$\Delta I_L = I_L - I_{L0} = \frac{V_{in} - V_{out}}{L} \times \alpha T \dots\dots\dots(1)$$

This proportionality between the current ripple and the duty cycle prove that ΔI_L can be controlled by α .

For OFF state, we have a first order differential equation (the inductor current flows through the diode):

$$L \times \frac{dI_L(t)}{dt} = -V_{out}$$

The solution of the differential equation yields:

$$I_L(t) = \frac{-V_{out}}{V_L} \times t + I_L$$

Now, for $t \rightarrow T_{OFF} = (1 - \alpha) \times T$, the inductor current decreases to its minimum Value I_{L0} therefore:

$$I_{L0} = \frac{-V_{out}}{V_L} \times (1-\alpha) \times t + I_L$$

So, we can obtain the equation of the current ripple:

$$\Delta I_L = I_L - I_{L0} = \frac{V_{on}}{L} \times (1-\alpha) \times T \dots\dots\dots(2)$$

By a simple comparison between (1) and (2) it is obvious that:

$$V_{out} = \alpha \times V_{in}$$

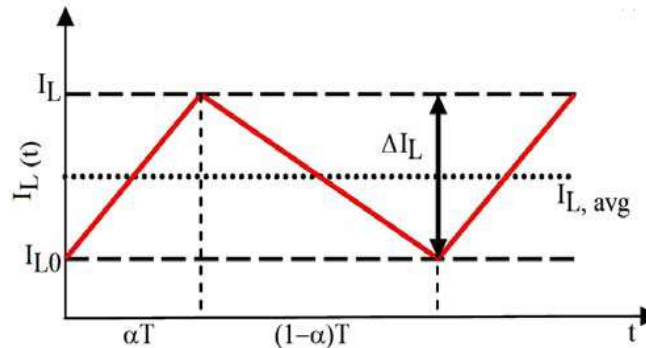


Figure:5 Inductor current waveform

To obtain the average inductor current, we can use:

$$I_{L,avg} = \frac{V_{out}}{R}$$

Therefore, the maximum and the minimum current through the inductor are:

$$I_{L,max} = I_{L,avg} + \frac{\Delta I_L}{2}$$

$$I_{L,max} = \frac{V_{out}}{R} + \frac{V_{out}}{2L} (1-\alpha)t$$

$$I_{L,min} = I_{L,avg} - \frac{\Delta I_L}{2}$$

$$I_{L,min} = \frac{V_{out}}{R} - \frac{V_{out}}{2L} (1-\alpha)t$$

We can now with the same analysis obtain the maximum and minimum current Through the capacitor:

$$I_{c,max} = \frac{\Delta I_L}{2} = \frac{V_{out}}{2L} \times (1-\alpha) \times t$$

$$I_{c,min} = -\frac{\Delta I_L}{2} = -\frac{V_{out}}{2L} \times (1-\alpha) \times t$$

The waveform for the current through the capacitor is shown below:

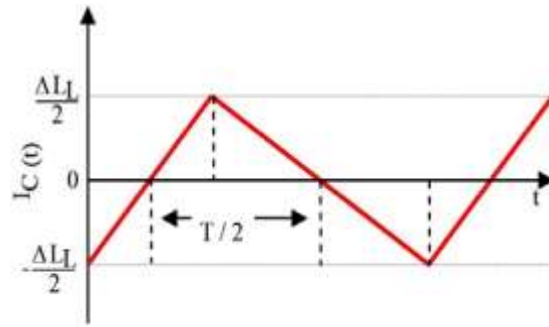


Figure:6 Capacitor current waveform

From Figure 3, it's clearly that the average current through the capacitor is zero, but in one-half cycle the capacitor will charged and the increase in charge is:

$$\Delta Q = \frac{1}{2} \times \frac{I_L}{2} \times \frac{T}{2} = \frac{1}{8} \times \Delta I_L \times T \dots \dots \dots (3)$$

and because $V_c = V_{out}$, the increase in capacitor voltage is:

$$\Delta V_{OUT} = \frac{\Delta Q}{C}$$

Then from (2) and (3) we obtain:

$$\Delta V_{OUT} = \frac{1}{8L \times C \times f^2 \times V_{OUT}}$$

In continuous conduction mode (CCM), $I_{L,min} = 0$; (the minimum current can be zero at the time of switching), so from (12) we have:

$$\frac{V_{out}}{R} = \frac{V_{out}}{2L} \times (1 - \alpha) \times t$$

Then if the desired switching frequency f and load resistance R are established, the minimum inductor current required for CCM is:

$$L_{min} = \frac{R}{2f} \times (1 - \alpha)$$

Likewise we can obtain:

$$f_{min} = \frac{R}{2L} \times (1 - \alpha)$$

$$R_{min} = \frac{2f \times L}{(1 - \alpha)}$$

These equations are used for designing and simulating buck converter circuit.

SIMULINK MODEL

Simulink Model of PV array is shown in figure below which is then simulated to obtain result by using variable input radiation parameters:

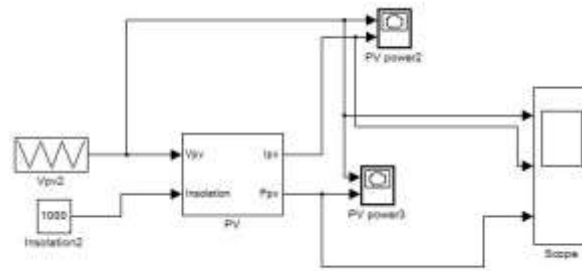


Figure 7: Simulink model of PV array

These three modes of DC-DC converters have been uniformly implemented in the MATLAB/Simulink as show in Figure 8.

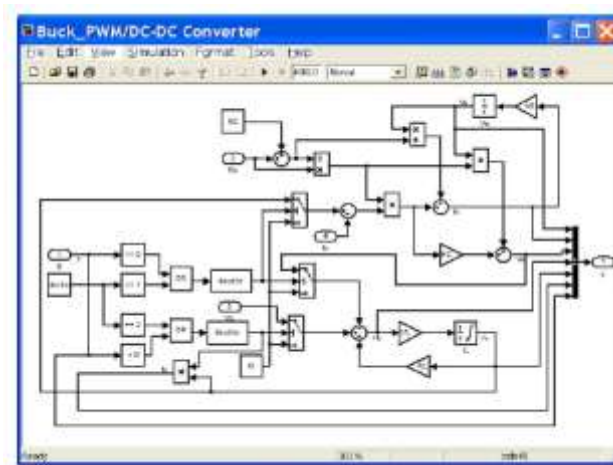


Figure 8. A uniform Simulink model of DC-DC converters.

The input-output connections of the model is shown in Figure 9. The first input to the model is the switch

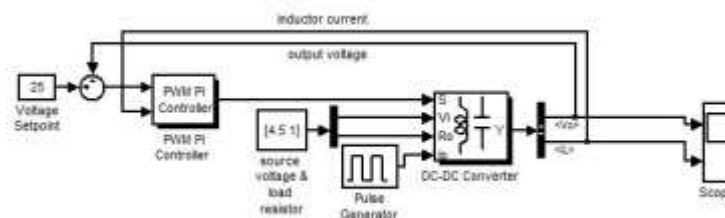


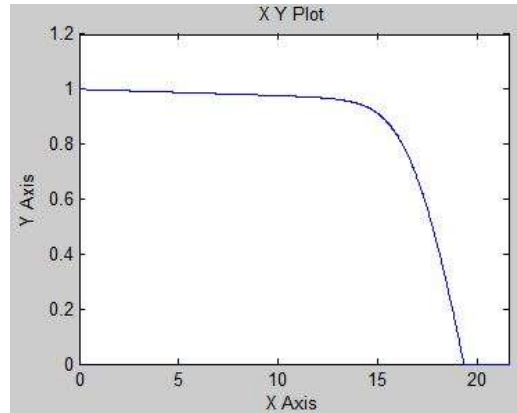
Figure 9: Input and output connections of the DC-DC converter model.

signal eight 1 or 0. The second one defines the DC source voltage and internal resistance. The third input is used to define the output current. The model has two outputs, the output voltage and the inductor current, which are the states of the system. The model can be configure with a number of parameters. These parameters are: the capacitance, C, inductance, L, the internal resistance of the capacitor and the inductor, RC and RL respectively. Three converter modes can be selected through the pull-down menu. One can also define either zero or non-zero value to the initial capacitor voltage by selecting or de selecting the “zero capacitor voltage” option. Finally, the option “Positive Inductor Current” defines whether the condition $I_L \geq 0$ should be enforced or not.

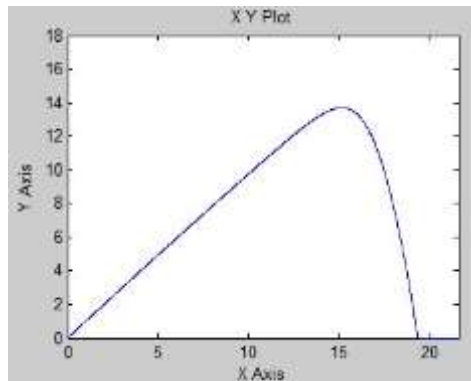
SIMULATION RESULTS

After simulating the above equations and above models, following results can be obtained:

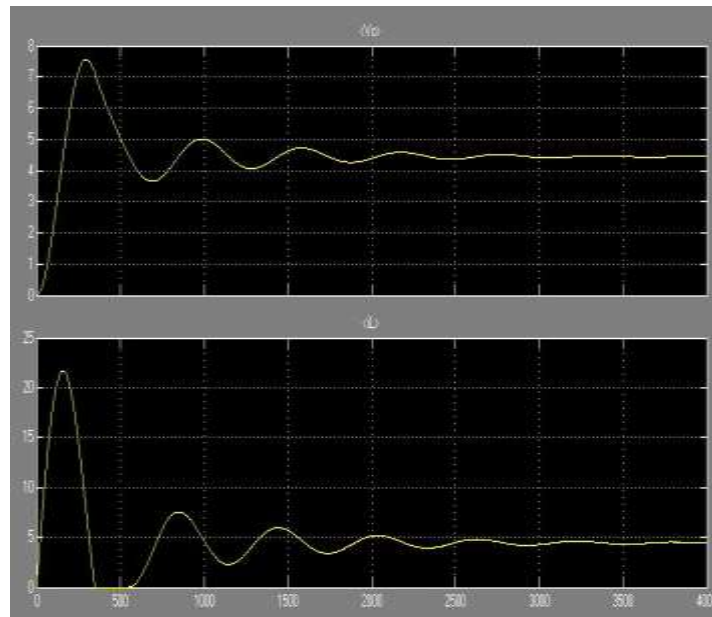
Current Vs Voltage of PV array



Power Vs Voltage of PV array.



Output of Buck Converter



CONCLUSION

Thus in this paper, a buck converter is designed and simulated successfully to obtain the constant output of about 4.5V integrated with mobile charging circuit to charge the mobile. From simulation results, it can be seen that by using buck converter, we were able to convert variable voltage and current into a constant one. As the solar panel provides variable current and voltage depending upon the solar radiations, we used PWM MPPT technique to obtain continuous maximum power output which is then given input to the buck converter. Our further research work will be in comparative study of converters namely Buck, Boost and Cuk converter.

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